



Article Adaptive Sensorless PI+Passivity-Based Control of a Boost Converter Supplying an Unknown CPL

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Abstract: This paper presents an adaptive control to stabilize the output voltage of a DC–DC boost converter that feeds an unknown constant power load (CPL). The proposed controller employs passivity-based control (PBC), which assigns a desired system energy to compensate for the negative impedance that may be generated by a CPL. A proportional-integral (PI) action that maintains a passive output is added to the PBC to impose the desired damping and enhance disturbance rejection behavior, thus forming a PI+PBC control. In addition, the proposed controller includes two estimators, i.e., immersion and invariance (I&I), and disturbance observer (DO), in order to estimate CPL and supply voltage for the converter, respectively. These observers become the proposed controller for an adaptive, sensorless PI+PBC control. Phase portrait analysis and experimental results have validated the robustness and effectiveness of the adaptive proposed control approach. These results show that the proposed controller adequately regulates the output voltage of the DC–DC boost converter under variations of the input voltage and CPL simultaneously.

Keywords: passivity-based control; Hamiltonian function; asymptotic stability convergence; sensorless control design; adaptive control design; unknown constant power load

MSC: 93-02

1. Introduction

1.1. General Context

Recent advances in electrical distribution networks with DC technologies for medium and low-voltage applications have boosted the massive integration of multiple distributed energy resources, such as renewable generation [1], energy storage systems [2], and controllable loads [3], among others. The main characteristic of integrating these devices in DC networks is the need to use power electronic converters to manage their behavior and take each one of them to an optimal operating point [4]. Figure 1 presents most of the typical DC–DC converters used to interface distributed energy resources and controllable loads to a DC bus.

Note that the technology of the converter will depend exclusively on the distributed energy resource it interfaces. In the case of solar and wind sources, the converter is unidirectional, and it may be a buck or boost converter [5]. Battery energy storage systems must be bidirectional in nature, as the battery behaves as a load in some periods and as a power supply in others [6]. For this reason, bidirectional boost converters constitute an



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Copyright: © 2022 by the authors. Licensee MDPI, Basel, Switzerland. This article is an open access article distributed under the terms and conditions of the Creative Commons Attribution (CC BY) license (https:// creativecommons.org/licenses/by/ 4.0/). alternative to manage energy behavior in batteries. In the case of controllable loads (linear or nonlinear), the energy flow goes from the DC bus to the load, which implies that a buck or boost converter can be used to integrate them into the DC network.



Figure 1. Some classical converters employed to interface distributed energy resources and loads in DC networks.

In Figure 1, it is evident that power electronic converters play the most important role in the massive integration of distributed energy resources and loads into DC networks. This implies that advanced control techniques are required to manage energy requirements effectively.

1.2. Motivation

The power electronic converters presented in Figure 1 pose important challenges to the operation of entire DC grids, given that efficient methodologies are needed to control the DC system at primary, secondary, and tertiary levels. The primary control design is the first layer that associates the system's behavior with its physical devices, i.e., this control stage is entrusted with operating each converter. It is necessary to consider the system's physical requirements regarding its response speed while ensuring a stable behavior under normal operating conditions [7]. The secondary control is also known as restorative control, which aims to stabilize the operation of the DC network under abnormal conditions, i.e., temporary short-circuit or load disconnections, with the main purpose of preserving all the state variables within a secure range of operation [8]. On the other hand, the tertiary control stage, also known as the optimization stage, is entrusted with defining the operative conditions of the network (signal references) in order to minimize or maximize some performance indicators [9].

The main interest of this research corresponds to the primary control design for a converter that interfaces the DC network with an energy user. Specifically, it focuses on designing a controller for a boost converter to support the voltage profile of a constant power load at the terminals while assuming that the voltage of the DC network and the value of the constant power consumption are unknown. This is a critical problem in microgrid energy management systems, as it is necessary to design an efficient controller that ensures the stable operation of the load and grid under normal operating conditions [10]. However, this is a challenging control task, given the nonlinearities introduced by the load (negative impedance) and that the boost converter has a nonlinear model [11,12]. Due to the above, it is important to study nonlinear controls that include estimators for the external inputs to the system, guaranteeing its stability [13–15].

1.3. Literature Review

Multiple studies on control methods implemented in boost converters have been presented in the specialized literature. Stability analysis for a boost converter supplied a constant power load (CPL) was proposed by [16]. A robust sliding mode control based on pulse-width modulation was described in [17] in order to remove possible instabilities provided by the CPL in DC microgrids. An adaptive backstepping sliding mode control to regulate the output voltage of a boost converter connected to a CPL was proposed in [18]. A sliding mode control to manage the output voltage of a boost converter feeding a CPL was presented in [19], where the authors employed a switching surface that relieves the inrush current in the boost converter and external disturbances by maintaining its output voltage at the desired value. Incremental passivity-based control (PBC) was presented in [20,21] to stabilize the output voltage of a DC–DC converter under time-varying disturbances, which were addressed by implementing a proportional-integral (PI) observer. In [22], a robust type-II fuzzy technique based on pulse-width modulation was presented to control a DC–DC boost converter with a CPL. The authors of [23] designed a controller to regulate the output voltage of a DC–DC boost converter feeding a CPL. This controller was based on a sliding mode control method and included a finite-time parameter observer. An adaptive output feedback control to maintain the output voltage of a DC–DC boost converter was shown in [24]. This control added an estimate for the converter's inductor current and load conductance based on a reduced-order state observer. The study by [25] designed a PI-PBC method to ensure that the output voltage of a DC–DC boost converter remained at the desired value. This method included a parameter estimation-based observer for the converter inductor current. Finally, the authors of [26] presented a nonlinear control based on the interconnection and damping assignment (IDA) PBC strategy to regulate the output voltage of a boost-type DC–DC converter.

1.4. Contribution and Scope

Considering the aforementioned literature review, this research article makes the following contributions:

- i. The formulation of a general nonlinear control design based on PBC theory, which regulates the voltage at the terminals of an unknown constant power load fed by a boost converter.
- ii. The addition of a PI design that maintains passive output to improve the convergence of the proposed control and remove the oscillations generated by the disturbance.
- iii. The combination of the immersion and invariance (I&I) and disturbance observer techniques to estimate the CPL and input voltage of the converter with the proposed controller, thus making it an adaptive, sensorless PI+PBC control, as verified by the simulation and experiment results.

The main advantage of the proposed PBC design, which includes PI action, is that it ensures the asymptotic stable operation of the boost converter, taking into account that its input voltage, as well as the CPL values, are estimated in real-time (online). This is particularly important because the control law in closed-loop operation is independent of physical measures, namely the voltage input and load current. This approach reduces the number of sensors required.

1.5. Document Organization

The remainder of this document is structured as follows: the mathematical modeling of the DC–DC boost converter using averaging modeling theory and control problem formulations is presented in Section 2. The design of the proposed adaptive controller with the inclusion of the estimator is described in Section 3. Section 4 presents the phase portrait analysis and experimental results used to validate the proposed controller. Finally, Section 5 lists the main conclusions of this research.

2. Mathematical Modeling and Problem Formulation

This section uses averaging modeling theory to describe the general mathematical modeling of the boost converter feeding a constant power load. It also presents the equilibrium point for this system, which is essential in designing any control approach. In addition, the control problem formulation and the requirements for the voltage regulation of the studied converter are defined.

2.1. DC–DC Boost Converter Modeling

A boost converter is a DC–DC converter whose voltage output has a higher DC value than its input voltage, which makes it a step-up converter. A boost converter is composed of two semiconductor switches (diode and IGBT) and two elements for energy storage, namely the capacitor and the inductor [27]. Figure 2 illustrates a boost converter supplying a CPL. Its dynamic model is achieved using two of Kirchhoff's laws. The second law is applied at node, which connects the capacitor, inductor, and IGBT. In contrast, Kirchhoff's first law is applied to the closed-loop trajectory that contains the inductor, thus generating its dynamic model:

$$L\dot{i} = -(1-u)v + E,$$

$$C\dot{v} = (1-u)i - \frac{P}{v},$$
(1)

where *i*, *v*, *E* \in *R*_{>0} are the inductor current, output voltage, and input voltage, respectively. *P* \in *R*_{>0} is the CPL, *u* \in [0,1] is the control input, and *L*, *C* \in *R*_{>0} are inductance and capacitance values, respectively.



Figure 2. Scheme of a DC–DC boost converter supplying a CPL.

The determination of the equilibrium point for the boost converter (1) is straightforward:

$$\varepsilon := \left\{ (i, v) \in R_{>0}^2 \mid iE - P = 0 \right\}.$$
 (2)

2.2. Control Problem Formulation

The control challenges for the dynamic model (1) lie in the extracted power load P and input voltage E, which are assumed to be unknown. Hence, the aims of the proposed controller are:

- i. To design a control law in order to regulate the output voltage v at the desired equilibrium point v^* ;
- ii. To develop an observer to estimate the value of the CPL which achieves an adaptive control;
- iii. To propose an estimator for the input voltage *E* in order to obtain a sensorless control scheme.

For the sake of simplicity, defining $x_1 := i$, $x_2 := v$ yields the following:

$$L\dot{x}_{1} = -(1-u)x_{2} + E,$$

$$C\dot{x}_{2} = (1-u)x_{1} - \frac{P}{x_{2}}.$$
(3)

The assignable equilibrium set for the dynamic model (3) can be expressed as

$$\varepsilon_x := \Big\{ (x_1, \, x_2) \in R_{>0}^2 \mid x_1 E - P = 0 \Big\}.$$
(4)

Hence, for a given $x_{2\star}$ in (4), the desired equilibrium point for $x_{1\star}$ is

$$x_{1\star} = \frac{P}{E}.$$
(5)

Remark 1. The main challenges in designing a controller to regulate the voltage output for a boost converter feeding a constant power terminal are the nonlinearities caused by the product between control inputs and state variables—which generates a bilinear system—and the presence of the CPL. This generates a negative impedance in the normal operation of the converter, which may lead to instabilities if it is not considered in the control design [28].

3. Adaptive PI+PBC Design

PBC is a well-founded theory that is supported by Lyapunov analysis and exploits the advantages of the port-Hamiltonian modeling of physical systems to design closed-loop controllers that maintain the pH structure of the system by modifying their internal energy behavior [29]. There are multiple approaches based on PBC theory, such as standard PBC design [30], interconnection and damping assignment (IDA-PBC) [26], the energy shaping PBC approach [31], and PBC with PI gains for purely bilinear systems [27]. The nature of the open-loop pH model determines the selection of a particular PBC theory to design a controller for a physical system.

In this research, the proposed controller is designed under the following considerations:

- i. The design of a PI-PBC control that guarantees locally asymptotically stability at desired equilibrium point $(x_{1\star}, x_{2\star})$ is described while assuming *P* and *E* as known parameters;
- ii. The immersion and invariance (I&I) technique is implemented to estimate the unknown CPL;
- iii. The proposed controller includes a nonlinear disturbance observer (DO) to observe the input voltage *E*;
- iv. By incorporating the I&I and DO techniques into the proposed controller, an adaptive sensorless PI+PBC control scheme is reached.

The dynamic system (3) can be presented as an Euler–Lagrange (EL) structure [29]:

$$M\dot{x} + (J(1-u) + R(x))x = \zeta,$$
(6)

where $x = [x_1, x_2]^{\top}$ is the state variable; and $M > 0 \in R^{2 \times 2}$, $R \ge 0 \in R^{2 \times 2}$, and $J = -J^{\top} \in R^{2 \times 2}$ are the generalized inertia (matrix associated with the energy storage devices in the converter), damping, and interconnection matrices, respectively. These matrices are represented as

$$M = \begin{bmatrix} L & 0 \\ 0 & C \end{bmatrix}, R = \begin{bmatrix} 0 & 0 \\ 0 & \frac{P}{x_2^2} \end{bmatrix}, J = \begin{bmatrix} 0 & 1-u \\ -(1-u) & 0 \end{bmatrix}, \zeta = \begin{bmatrix} E \\ 0 \end{bmatrix}.$$

The energy function H(x) of the dynamic system (6) is

$$H(x) = \frac{1}{2}x^{\top}Mx,\tag{7}$$

which is a positive definite function.

Remark 2. Note that the differentiation with respect to time in (7) accomplishes the following power balance equation:

$$\dot{H}(x) = \underbrace{Ex_1}_{Input \ power} - \underbrace{P}_{Output \ power}, \tag{8}$$

which shows that the difference regarding the change in stored energy is equal to the difference between the input and output power.

3.1. PI+PBC Design

This subsection presents the general passivity-based control design for the converter model using a general Hamiltonian function that moves the equilibrium point to the desired operating point by ensuring asymptotic stability in closed-loop operation. The addition of the integral action of the PBC feedback control law is also described in detail.

3.1.1. PBC Design

The proposed controller is based on a PBC methodology [29] that stabilizes the dynamic model (6).

Theorem 1. Let us assume that the dynamic model (6) is controlled with the control input

$$u_{PBC} = \frac{x_1 \left(x_{1\star} - \frac{P}{x_2} + P \frac{x_2 - x_{2\star}}{x_2^2} \right) - x_2 (E - x_{2\star})}{x_1^2 + x_2^2}.$$
(9)

Therefore, the dynamic model (6) *is locally stable in closed-loop.*

Proof. First, the error is defined as $e : x - x_*$, and the dynamic model (6) is proposed as follows in closed loop:

$$M\dot{e} + (J + R(x))e = 0,$$
 (10)

where x_{\star} is constant, so $\dot{e} = \dot{x}$.

By proposing the desired stored energy function in closed loop, the dynamic model (6) is expressed as

$$H(e) = \frac{1}{2}e^{\top}Me.$$
 (11)

Taking its derivative with respect to the time along the trajectory (10) yields (6) as

$$\dot{H}(e) = -e^{\top} (J + R(x))e,$$

$$= -e^{\top} Je - e^{\top} R(x)e$$

$$= -e^{\top} R(x)e,$$

$$\leq 0$$
(12)

which implies that the system is passive.

Now, by subtracting (10) in (6), the following equation is achieved:

$$(J(1-u) + R(x))x - (J + R(x))e = \zeta.$$
(13)

Alternatively, Equation (13) can be expressed as

$$G(x)u + (J + R(x))x - (J + R(x))e = \zeta,$$
(14)

with $G(x) = [-x_2, x_1]^{\top}$, and its full-rank left annihilator $G(x)^{\perp} = [x_1, x_2]$, which meets $G(x)^{\perp}G(x) = 0$. Multiplying $G(x)^{\perp}$ in (14) yields

$$x_{1\star} = \frac{Px_{2\star} + x_1 x_2 (x_{2\star} - E)}{x_2^2}.$$
 (15)

The proposed PBC is obtained by solving (14) as

$$u_{PBC} = [G(x)^{\top}G(x)]^{-1}G(x)^{\top}(J+R(x))e - (J+R(x))x + \zeta)$$

=
$$\frac{x_1\left(x_{1\star} - \frac{P}{x_2} + P\frac{x_2 - x_{2\star}}{x_2^2}\right) - x_2(E - x_{2\star})}{x_1^2 + x_2^2},$$
(16)

which completes the proof. \Box

3.1.2. PI Design

A PI controller was added to the proposed control law (16) in order to ensure that the closed-loop system is locally asymptotically stable.

Theorem 2. A PI controller is introduced into the dynamic system (6) as

$$u_{PI} = -K_p G(x)^{\top} e - K_i G(x)^{\top} z$$

$$\dot{z} = e,$$
(17)

where $K_v, K_i \in \mathbb{R}^{2 \times 2} > 0$ are proportional and integral diagonal matrices, respectively.

Now, the closed-loop system takes the following form:

$$M\dot{e} + (J + R(x))e = u_{PI}.$$
 (18)

Analyzing the derivative with respect to the time of the desired stored energy function (11) along the trajectory (18) yields

$$\dot{H}(e) = -e^{\top} (J + R(x))e + e^{\top} u_{PI}$$

$$= -e^{\top} J(u)e - e^{\top} R(x)e + e^{\top} u_{PI}$$

$$= e^{\top} R(x)e + e^{\top} u_{PI}$$

$$< e^{\top} u_{PI},$$
(19)

which implies that the map $u_{PI} \rightarrow e$ is passive according to H(e) (for more details, see [32]). Therefore, the closed-loop system (18) is locally asymptotically stable with the Lyapunov function

$$W(e,z) = H(e) + \frac{1}{2}K_i z^{\top} z.$$
 (20)

Proof. By defining $\chi = [x, z]^{\top}$, the closed-loop system (18), represented as an EU structure, can be expressed as

$$\begin{bmatrix} M & 0 \\ 0 & K_i \end{bmatrix} \dot{\chi} + \begin{bmatrix} J + R(x) + G(x)K_pG(x)^\top & G(x)K_iG(x)^\top \\ -G(x)K_iG(x)^\top & 0 \end{bmatrix} \chi = 0.$$
(21)

By using the candidate Lyapunov function (20) and taking its deviate with respect to time along the trajectory (21), the following is obtained:

$$\dot{W}(e,z) = \dot{H}(e) + k_i \dot{\chi} \chi$$

$$< e^{\top} u_{PI} + k_i \dot{\chi} \chi$$

$$= -K_p e^{\top} e - K_i e^{\top} \chi + K_i e^{\top} \chi$$

$$= -K_p e^{\top} e < 0.$$
(22)

Invoking the LaSalle–Yoshizawa theorem [33], the closed-loop system (21) is locally asymptotically stable as long as

$$\lim_{t \to \infty} \chi(t) = 0. \tag{23}$$

 $e \rightarrow 0$ is fixed, hence e = 0 in (21), which implies that $z \rightarrow 0$. \Box

3.2. CPL Estimator

The proposed PI+PBC control requires knowing the CPL to compute the control law, and the CPL is usually unknown. This study employs an immersion and invariance (I&I) technique for estimating said load.

Theorem 3. The load P in the dynamic system (3) is estimated with

$$\hat{P} = \alpha + \gamma \beta(x_2)$$

$$\dot{\alpha} = -\gamma \beta'(x_2) \left(\frac{1-u}{C}x_1 - \frac{\hat{P}}{Cx_2}\right),$$
(24)

where $\gamma > 0$ is the gain of the I&I technique. By denoting the estimation error as

$$\tilde{P} = \hat{P} - P, \tag{25}$$

where \tilde{P} is the estimation error of CPL and \hat{P} is its estimation, the following is obtained:

$$\lim_{t \to \infty} \tilde{P}(t) = 0. \tag{26}$$

Proof. Taking the derivative of the estimation error (25) with respect to time yields the following result:

$$\dot{\tilde{P}} = \dot{\tilde{P}} = \dot{\alpha} + \gamma \beta'(x_2) \dot{x}_2$$

$$= \dot{\alpha} + \gamma \beta'(x_2) \left(\frac{1-u}{C}x_1 - \frac{P}{Cx_2}\right)$$

$$= \dot{\alpha} + \gamma \beta'(x_2) \left(\frac{1-u}{C}x_1 - \frac{\hat{P} - \tilde{P}}{Cx_2}\right).$$
(27)

Now, by substituting $\dot{\alpha}$ in (28), the following result is achieved

$$\dot{\tilde{P}} = \gamma \beta'(x_2) \frac{\tilde{P}}{Cx_2}.$$
(28)

Now, it is necessary to define $\beta(x_2)$ in order to ensure that the convergence of \hat{P} will be exponential, which is defined as

$$\beta(x_2) = -\frac{1}{2}Cx_2^2,$$
(29)

and its time derivative is

$$\beta'(x_2) = -Cx_2. \tag{30}$$

By replacing (30) in (28), the following expression is obtained:

$$\tilde{P} = -\gamma \tilde{P} \Rightarrow \tilde{P}(t) = \tilde{P}(0)e^{-\gamma t}.$$
(31)

In (31), it can be noted that $\tilde{P}(t)$ will exponentially tend to zero for all initial conditions. \Box

3.3. Input Voltage Estimator

Theorem 4. For the system (3), a DO technique to estimate input voltage is designed as follows:

$$\hat{E} = \zeta + \rho x_1
\dot{\zeta} = -\frac{\rho}{L} (\zeta + \rho x_1 - (1 - u) x_2),$$
(32)

where $\rho > 0$ is the gain of the DO technique. Defining the estimate error as $\tilde{E} = \hat{E} - E$ yields

$$\lim_{t \to \infty} \tilde{E}(t) = 0.$$
(33)

Proof. By taking the derivative \tilde{E} concerning time along the trajectories (3) and (32), the following is obtained:

$$\dot{\tilde{E}} = \dot{\tilde{E}} = \dot{\zeta} + \rho \dot{x}_1
= -\frac{\rho}{L} \tilde{E} \Rightarrow \tilde{E}(t) = \tilde{E}(0) e^{-\frac{\rho}{L}t},$$
(34)

which satisfies the convergence property in (33).

3.4. Adaptive Sensorless Control Design

By replacing the estimates \hat{P} of (24) and \hat{E} of (32) into (16), (15), and (17), the proposed adaptive sensorless control takes the following form:

$$u = \hat{u}_{PBC} + \hat{u}_{PI} = \frac{x_1 \left(\hat{x}_{1\star} - \frac{\hat{P}}{x_2} + \hat{P} \frac{x_2 - x_{2\star}}{x_2^2} \right) - x_2 (\hat{E} - x_{2\star})}{x_1^2 + x_2^2} + \hat{u}_{PI},$$

$$\hat{u}_{PI} = -K_p G(x)^\top \hat{e} - K_i G(x)^\top \hat{z},$$

$$\hat{e} = [x_1 - \hat{x}_{1\star}, x_2 - x_{2\star}]^\top,$$

$$\hat{z} = \hat{e},$$

$$\hat{x}_{1\star} = \frac{\hat{P} x_{2\star} + x_1 x_2 (x_{2\star} - \hat{E})}{x_2^2}.$$

(35)

4. Results

This section presents the performance of the controller described in Section 3. The adaptive sensorless PI+PBC has been designed to stabilize the output voltage in a boost converter supplying an unknown CPL. Phase portrait analysis and experimental results are employed to evaluate the dynamic behavior of the proposed controller. The boost converter prototype's list of components and values is presented in Table 1.

Component	Description	Type/Value
Q_1	Power MOSFET	IRFB4110
D_1	Schottky Power Diode	RURG8060
L	Inductor	Wurth Elektronik 74435584700, 47 μH
С	Multilayer Ceramic Capacitor	TDK C5750X7S2A106M230KB, $10\times 10~\mu F$

Table 1. Description of the boost converter's components.

The RT-Box of Plexim was used to implement the proposed controller with a time sample of 10 μ S. Figure 3 depicts the implemented prototype system. The gains of the adaptive controller were tuned online via of the RT-box of Plexim. These gains are: $k_{p1} = 0.2$, $k_{p2} = 0.05$, $k_{i1} = 0.4$, $k_{i2} = 5$, $\gamma = 0.1$ and $\rho = 2$.



Figure 3. Experimental setup: (a) oscilloscope, (b) DC electronic device in CPL mode, (c) RT-Box with analog and digital breakout boards, (d) current probe power supply, (e) MOSFET driver power supply, (f) DC–DC boost converter, (g) current probes, (h) input voltage power supply, and (i) differential voltage probe.

The phase portrait for the boost converter implemented with the proposed controller is shown in Figure 4. The desired equilibrium point (\bigstar) for the boost converter is calculated with E = 10 V, P = 20 W, $x_{1*} = 2$ A, and $x_{2*} = 15$ V. Figure 4 also shows five trajectories for different initial points. It can be observed that the state variables move in ranges $1.5 \text{ A} \le x_1(0) \le 4 \text{ A}$ and $6.5 \text{ V} \le x_2(0) \le 17 \text{ V}$. According to the figure, if $x_2(0) < x_{2*}$, x_1 initially increases above its equilibrium point, while $x_2(0)$ goes near the desired equilibrium point; meanwhile, if $x_2(0) > x_2^*$, x_1 and x_2 converge directly to their equilibrium points.

Figure 5 illustrates the estimation of the input voltage when the CPL has a constant value (P = 20 W). Figure 5a presents the dynamic response of the estimate \hat{E} (blue line) when the input voltage (yellow line) increases from 10 to 8 V. In contrast, Figure 5b reveals the estimate \hat{E} (blue line) when the input voltage (yellow line) decreases from 10 to 12 V. These figures show that the estimation of the input voltage \hat{E} can be validated and that its convergence rate is very fast.







Figure 5. Dynamic response of the estimate \hat{E} : (**a**) input voltage changes between 10 to 8 V; and (**b**) input voltage changes between 10 to 12 V. CH1: (1 V/div), CH3: (1 V/div).

Figure 6 depicts the experimental response of the boost converter while considering that the CPL varies between 20 and 40 W like a 100 Hz square waveform. In this case, it is also considered that the desired output voltage is $x_{2\star} = 15$ V, and the input voltage for the boost converter varies from 10 and 8 V at the same time. Figure 6a shows the output voltage x_2 (yellow line), the inductor current x_1 (green line), the control signal u (blue line),



and the estimate \hat{P} (purple line). Figure 6b depicts the input voltage *E* (yellow line) and the estimate \hat{E} (blue line).

Figure 6. Dynamic response of the proposed controller: (**a**) Experimental results for the boost converter when the CPL is a 100 Hz square waveform between 20 and 40 W, with a duty cycle of 0.5. CH1: x_2 (3 V/div), CH2: x_1 (2 A/div), CH3: u (1/div), CH4: \hat{P} (20 W/div), and time base of 5 ms. (**b**) Input voltage changes from 10 V to 8 V. CH1: (1 V/div), CH2: (1 V/div), and time base of 10 ms.

In Figure 6a, it can be observed that the adaptive sensorless PI+PBC control can instantly stabilize the output voltage of the boost converter. The settling time for the output voltage is 1.53 ms, and its average overshoot is 5.1%, while the inductor current has a settling time of around 0.171 ms and no overshoot. This demonstrates the adequate performance of the proposed controller under simultaneous CPL and input voltage variations. Additionally, it is observed that the inductor current x_1 (green line in Figure 7a) increases when the input voltage *E* decreases. This behavior is expected, given that the balance point for the inductor current depends inversely on the input voltage, as presented in (5) (hyperbolic relation between voltage and current in the presence of a CPL).

Figure 7 presents the experimental response of the boost converter when its input voltage changes from 10 V to 12 V, and the CPL varies between 20 and 40 W like a 100 Hz square waveform simultaneously. The desired output voltage remains the same $x_{2\star} = 15$ V. Figure 7a illustrates the output voltage x_2 (yellow line), the inductor current x_1 (green line), the control signal u (blue line), and the estimate \hat{P} (purple line). Figure 7b shows the input voltage E (yellow line) and its estimation \hat{E} (blue line).





It can be seen in Figure 7a that the adaptive sensorless PI+PBC control instantly continues to regulate the output voltage of the boost converter under the changes considered. This is supported by the fact that the settling time for the proposed controller is 1.53 ms, and its average overshoot is 5.1%. It is worth mentioning that, as expected, when the voltage input increases, the total current flowing through the inductor decreases since its movement is required to ensure constant power transference from the source to the load.

General Remarks

From the experimental validation presented for a boost converter feeding an unknown CPL while using the proposed controller, it is possible to observe that:

i. The presented estimator to determine the behavior of the voltage input has an exponential convergence to the exact value when the behavior of the constant power load remains constant (see Figure 5), regardless of whether the voltage input increases or decreases from an initial value. Notwithstanding, when the load varies with a square form and the voltage input also increases or decreases, the behavior of the voltage input estimator follows the average behavior of the input, albeit with square-form oscillations (see Figures 6b and 7b), which is expected because the DO estimator presented in Section 3.3 is dependent on the current measured at the inductor, which is also a function of the current provided to the load.

- ii. The load estimator presented in Figures 6a and 7a converges exponentially to the exact value, as predicted by the I&I method presented in Section 3.2. This is also expected since the estimator depends only on the voltage measured at terminals of the load, which is the control variable that remains constant, with small variations each time the load changes.
- iii. In general, the proposed PI+PBC approach demonstrated easy tuning characteristics (two control gains), and fast asymptotic convergence to the desired voltage reference, regardless of whether the load current and the voltage input are measured or estimated. These characteristics make the proposed PI+PBC structure a robust control approach that deals with voltage control in the face of unknown CPLs, thus reducing the number of sensors required in the physical implementation layer.

5. Conclusions

This paper presented the design of an adaptive control to regulate the output voltage of a DC–DC boost converter supplying an unknown CPL. The proposed controller used PBC theory to stabilize the output voltage at its desired value, and a PI action was added to accelerate its convergence. The PI action maintained a passive output, essential for PBC theory, and injected the desired damping, thus enhancing disturbance rejection. Additionally, the proposed controller added two observers, which allowed it not to depend on some parameters that, in general, can be difficult to measure. The I&I and DO techniques turned the controller into an adaptive sensorless PI+PBC control, whose robustness and effectiveness were evaluated by employing phase portrait analysis and experimental results. The whole set of these tests showed its ability to regulate and maintain the output voltage of the DC–DC boost converter at its desired values.

Some possible future works derived from this research may include: (i) extending the proposed control design and observers to classical second-order DC–DC converters (buck, buck-boost, and non-inverting buck-boost topologies) feeding CPLs, (ii) implementing inverse optimal control with integral action for controlling DC–DC converters in microgrid applications, Developing a sliding mode control with the I&I and DO techniques makes it more robust and allows the system to have a faster convergence.

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